Fig. 3 shows a method of detecting and correcting data errors in an ESL's registers or memory in accordance with the present invention.

DETAILED DESCRIPTION

The present invention now will be described more fully with reference to the accompanying drawings, in which several presently preferred embodiments of the invention are shown. This invention may, however, be embodied in various forms and should not be construed as limited to the embodiments set forth herein. Rather, these embodiments are provided so that this disclosure will be thorough and complete, and will fully convey the scope of the invention to those skilled in the art.

Further details of an ESL system suitable for use in conjunction with the present invention are found in U.S. Patent Application Serial No. _10 044 021_ filed January 11, 200$\overset{2}{\not{1}}$ entitled "Methods and Apparatus for Performing Delta Updates of an Electronic Shelf Label", U.S. Patent Application Serial No. _10 044 610_ filed January 11, 200$\overset{2}{\not{1}}$ entitled "Methods and Apparatus for Intelligent Data Bedcheck of an Electronic Shelf Label", U.S. Patent Application Serial No. _10 044 020_ filed January 11, 200$\overset{2}{\not{1}}$ entitled "Methods and Apparatus for Reduced Electronic Shelf Label Power Consumption", now US Patent 6626359 U.S. Patent Application Serial No. _10 044 535_ filed January 11, 200$\overset{2}{\not{1}}$ entitled "Methods and Apparatus for Error Detection and Correction of an Electronic Shelf Label System Communication Error", U.S. Patent Application Serial No. _10 044 439_ filed January 11, 200$\overset{2}{\not{1}}$ entitled "Methods and Apparatus for Automatically Locating an Electronic Shelf Label", U.S. Patent Application Serial No. _10 044 440_ filed January 11, 200$\overset{2}{\not{1}}$ entitled "Methods and Apparatus for Conserving Battery Power in an Electronic Shelf Label System", U.S. Patent Application Serial No. _10 044 688_ filed January 11, 200$\overset{2}{\not{1}}$ entitled "Methods and Apparatus for Automatic

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